

# Oscillator JTP75HC(V) · (VC)TCXO



- precision temperature compensated crystal oscillator, 7.0 x 5.0 mm
- frequency stability of ± 50 ppb available
- temperature range up to -40°C ~ +105°C
- JTP75HCV with frequency tuning option
- for a Stratum 3 compliant version refer to JTS75HC(V)

# .CH Conflict

Ph

Pb free

pliant mir

## GENERAL DATA

ТҮРЕ		JTP75HC / JTP75HCV (HCMOS output)			
frequency range		9.60 ~ 50.0 MHz (see developed frequ.)			
frequency	at +25 °C (*1)	± 1.0 ppm max.			
tolerance / stability	after 2x reflow (*2)	± 0.5 ppm max.			
Stability	temperature (*3)	see table 1			
	supply voltage (*4)	$\pm$ 0.1 ppm max. (at V $_{\rm DC}$ $\pm$ 5%)			
	load change (*5)	± 0.1 ppm max. (at nom load ± 5%)			
	aging first year (*6)	± 1.0 ppm max. (at +25 °C)			
	aging per day (*7)	± 10.0 ppb max.			
	stability (ADEV)	0.1 ppb / 0.2 ppb (stabilities ≥ ±0.28 ppm)			
with τ = 1 s	ec (typ. / max.)	0.2 ppb / 0.5 ppb (stabilities < ±0.28 ppm)			
current con	sumption max.	10.0 mA			
supply voltage $V_{_{DC}}$		3.3 V (all ±5%)			
tempera-	operating	see table 1			
ture	operable	-40 °C ~ +105 °C			
	storage	-55 °C ~ +105 °C			
output	rise/fall time max.	8 ns (10 %> 90 % of V <sub>DC</sub> )			
	nominal load	15 pF			
	low / high level	0.4 V max. / $\mathrm{V}_{\mathrm{DC}}$ - 0.4 V min.			
start-up time max.		3.0 ms			
$\rm V_{c}$ frequ. tuning range JTP75HCV		examples in table 2 (ask for more options)			
$\rm V_{\rm c}$ frequ. tuning voltage JTP75HCV		examples in table 3 (ask for more options)			

For  $(*1) \sim (*7)$  please refer to definitions shown on the 2nd page of this datasheet

TABLE 1: FREQUENCY STABILITY CODE
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frequency stability		E	F*1	H*1	G*1	J*1
temperature code		± 0.5 ppm	± 0.28 ppm	± 0.20 ppm	± 0.10 ppm	± 0.05 ppm
-30 °C ~ +75 °C	G	0	0	0	0	0
-40 °C ~ +85 °C	Κ	0	0	0	0	0
-40 °C ~ +105 °C	Ρ	0	0	0	0	0

O available

\*1 frequency stability options F / H / G and J can be ordered as Stratum 3 compliant versions, see separate JTS75HC(V) datasheet

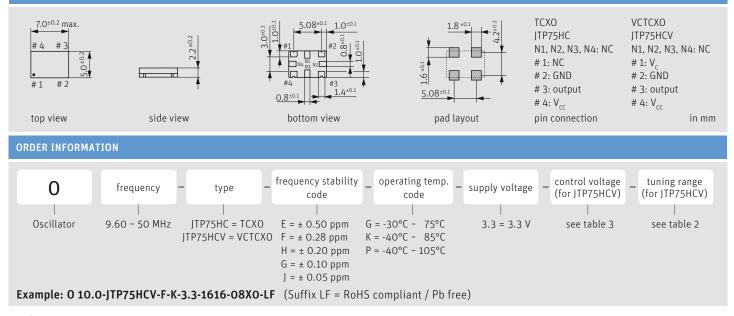
# TABLE 2: VC DEPENDENT FREQUENCY TUNING RANGE CODING METHOD

$V_{c}$ frequency tuning range	code	minimal	maximal
of JTP75HCV	05X0	± 5.0 ppm	undefined
table shows examples,	08X0	± 8.0 ppm	undefined
ask for more options	0510	± 5.0 ppm	± 10.0 ppm
	0812	± 8.0 ppm	± 12.0 ppm

### TABLE 3: VC CODING METHOD (EXAMPLES)

V <sub>c</sub> center voltage and	code	center of V <sub>c</sub>	range of V <sub>c</sub>			
$V_{\rm c}$ range	1616	1.65 V	± 1.65 V	1.65 V	± 1.65 V at V <sub>DC</sub> = 3.3 V	
	1610	1.65 V	± 1.00 V	1.65 V $\pm$ 1.00 V at V $_{\rm DC}$ = 3.3 V		
	1515	1.50 V	± 1.50 V	1.50 V $\pm$ 1.50 V at V $_{\rm DC}$ = 3.3 V		
	1510	1.50 V	± 1.00 V	1.50 V $\pm$ 1.00 V at V $_{\rm DC}$ = 3.3 V		
V <sub>c</sub> properties	input impedance of $V_c$ min.			100 k0hm		
	$V_{c}$ frequency tuning linearity max.			10 %		

#### DIMENSIONS





# Oscillator JTP75HC(V) · Precision TCXO & VCTCXO

PHASE NOISE INFORMATION						
phase noise at fO 19.2 MHz, V <sub>DC</sub> = 3.3 V @ +25 °C	at 10 Hz	-93 dBc/Hz typ.				
	at 100 Hz	-120 dBc/Hz typ.				
	at 1 KHz	-145 dBc/Hz typ.				
	at 10 KHz	-157 dBc/Hz typ.				
	at 100 KHz	-159 dBc/Hz typ.				

#### PACKAGING NOTE

non-multiple packing units are only supplied taped / bulk
 moisture sensitivity: MSL2

DEVELOPED FREQUENCIES						
all frequencies in MHz:	9.60	10.0	12.80	13.0	16.3840	
	18.4320	19.20	19.440	20.0	25.0	
	26.0	30.720	38.880	40.0	50.0	

#### NOTE

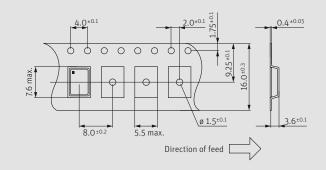
 for best supply noise rejection, connect a capacitor of 100nF and a second capacitor of 10μF closely to the supply voltage pins
 a separate voltage supply rail ensures best phase noise

- keep digital or high frequency signals as far away from  $V_c$  pin as possible

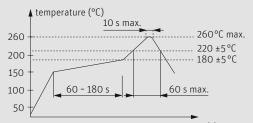
### DEFINITIONS

- \*1: Measured frequency observed with  $T_A = +25^{\circ}$ C and  $C_L = 15$  pF, at nominal  $V_{DC}$  and nominal center  $V_C$  (if applicable) within 30 days after ex-factory. The measured frequency is referenced to the specified nominal frequency.
- \*2: At specified reflow soldering profile, tested with T<sub>A</sub>=+25 °C and C<sub>L</sub>=15pF, at nominal V<sub>DC</sub> and nominal center V<sub>C</sub> (if applicable).
   At least 4 hours of static placement at room temperature is necessary after completion of 2 times reflow.
- \*3: T<sub>A</sub> varied in the specified operating temperature range, frequency variation is normalized to the middle point of whole frequency excursion, at nominal V<sub>DC</sub> and nominal center V<sub>c</sub> (if applicable), and at nominal output load, temperature variable speed less than 2°C per minute.
- \*4: Frequency variation if  $V_{DC}$  is varied by ± 5% of nominal  $V_{DC}$ , frequency variation is normalized to frequency observed at nominal  $V_{DC}$ , nominal center  $V_{C}$  (if applicable),  $T_{A}$ =+25 °C and nominal load.
- \*5: Frequency variation if the load is varied by ± 5% of nominal load, frequency variation is normalized to frequency observed at nominal V<sub>DC</sub>, nominal center V<sub>c</sub> (if applicable), T<sub>A</sub>=+25 °C and nominal load.
- \*6: The maximum 1st-year frequency deviation from the ex-factory status.  $T_A = +25$  °C, at nominal  $V_{DC}$ , nominal center  $V_C$  (if applicable),  $T_A = +25$  °C and nominal load. Normally, the largest frequency deviation occurs within the 1st year.
- \*7: The maximum frequency deviation within 24 hours in a steady state. The initial status acquired at  $T_A = +25$  °C, at nominal  $V_{DC}$ , nominal center  $V_C$  (if applicable), nominal load and after 1h of continuous operation.

### TAPING SPECIFICATION

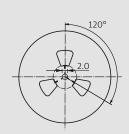


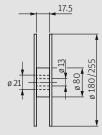
#### **REFLOW SOLDERING PROFILE**



time (s)

note: parts are also suitable for soldering systems with lead (Pb) content





in mm

#### MARKING

internal code (optional) / frequency dot / internal code

note: for more information please contact Jauch

